

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
1	BRS	L1	78282	clock near3 (frequency speed)	US-PGPUB; USPAT	2005/03/14 15:40		
2	BRS	L2	313243	(vary\$3 chang\$3 reduc\$3) near3 (frequency speed)	US-PGPUB; USPAT	2005/03/14 15:41		
3	BRS	L3	10961	1 same 2	US-PGPUB; USPAT	2005/03/14 15:44		
4	BRS	L4	20366	multiprocess\$3	US-PGPUB; USPAT	2005/03/14 15:44		
5	BRS	L5	28707	(multiple plurality) adj3 processor\$1	US-PGPUB; USPAT	2005/03/14 15:46		
6	BRS	L6	0	(intrgrated adj circuit) same (4 or 5)	US-PGPUB; USPAT	2005/03/14 15:47		
7	BRS	L7	1622	(integrated adj circuit) same (4 or 5)	US-PGPUB; USPAT	2005/03/14 15:50		
8	BRS	L8	2952	chip same (4 or 5)	US-PGPUB; USPAT	2005/03/14 15:53		
9	BRS	L9	87	3 and 7	US-PGPUB; USPAT	2005/03/14 15:53		
10	BRS	L10	121	3 and 8	US-PGPUB; USPAT	2005/03/14 15:53		
11	BRS	L11	155224	power near3 consum\$5	US-PGPUB; USPAT	2005/03/14 15:53		
12	BRS	L12	28	9 and 11	US-PGPUB; USPAT	2005/03/14 15:54		
13	BRS	L13	58	10 and 11	US-PGPUB; USPAT	2005/03/14 15:55		
14	BRS	L14	2208	713/300.ccls. 713/320.ccls.	US-PGPUB; USPAT	2005/03/14 15:55		
15	BRS	L15	1090	713/322.ccls. 713/323.ccls.	US-PGPUB; USPAT	2005/03/14 15:55		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
16	BRS	L16	1551	713/500.ccls. 713/501.ccls. 713/601.ccls.	US- PGPUB; USPAT	2005/03/14 15:56		
17	BRS	L17	4317	14 or 15 or 16	US- PGPUB; USPAT	2005/03/14 15:57		
18	BRS	L18	9	12 and 17	US- PGPUB; USPAT	2005/03/14 16:07		
19	BRS	L19	19	12 not 18	US- PGPUB; USPAT	2005/03/14 16:10		
20	BRS	L20	47	13 not 12	US- PGPUB; USPAT	2005/03/14 16:11		
21	BRS	L21	21	17 and 20	US- PGPUB; USPAT	2005/03/14 16:20		
22	BRS	L22	26	20 not 21	US- PGPUB; USPAT	2005/03/14 16:20		